



**BHARATI VIDYAPEETH INSTITUTE OF TECHNOLOGY
NAVI MUMBAI
ELECTRICAL ENGINEERING DEPARTMENT**

As per MSBTE directives, a State Level technical Paper Presentation Competition for students of Electrical Engineering Group of Mumbai region was organized by the department of Electrical Engineering, Bharati Vidyapeeth Institute of Technology, Navi-Mumbai on 10th of February 2026. The event was held in the Institute's Virtual Learning Center (VLC) hall. Mrs. Rajitha T.B HOD, Electrical engineering department welcomed the audience and briefed about the event. The function was graced with the presence of Prof. P.N Tandon, Principal who delivered inaugural address and Vice Principal Mr.J.K.Patil Sir conveyed his best wishes to the students for the competition. Comparing the event was done by Mrs. Kavita Sawant, Lecturer, Electrical Engineering department. Rules and guidelines of the competition was given by Mrs I.Srilakshmi Lecturer, Electrical Engineering department

Jury members for the Competition were Dr.P.N Phadgan from Govt.Poly,Mumbai and Mr.Shekar N.Ambekar from MSEDCL Kharghar.

During the valedictory function, participants and jury members spoke about their experience about the event and gave very positive feedback. The winners were presented with winner certificates and participation certificates were distributed to all the participants. At the end of the session, participants filled and submitted feedback forms. The competition concluded with the vote of thanks proposed by Mrs. Lovely Gaur, Lecturer, Electrical Engineering department.

Winners List

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| 1st Prize | Himanshu Borse and Bhavya Yadav from VPM Polytechnic, Thane – “AI in Smart Grid” |
| 2nd Prize | Vedika Pravin Lingayat and Shravani Sandip Shigavan from Rajaram Shinde Institute of Engineering and Technology – “AI electrical engineering” |
| 3rd Prize | Aaradhya M. Gaikwad and Yash R. Kadam from Shri Bhagubhai Mafatlal Polytechnic - “Green Hydrogen & its future in power sector” |

