

## Question Bank (G scheme)

Name of subject: EIM

Subject code : 17317

Semester : 3

Unit Test :2

Course : EJ/IS/IE

### Chapter 3 Digital Meters (10M)-

#### 3 marks

1. State the principle of dual slope type DVM with graph.(3 marks)
2. Draw the block dia. of LCR- Q meter.(3 marks)

#### **4 marks**

3. Draw block diagram of linear ramp type DVM and explain its working.
4. Draw block diagram of Staircase ramp type DVM and explain its working.
5. Draw & explain block diagram of dual slope type DVM.
6. State any four specification of DMM.

### **Chapter 4 Cathode Ray Oscilloscope(24M)**

#### **3marks**

7. Draw labeled dia. Of CRT.
8. State the role of time base generator in CRO.
9. Draw the block dia. Of Dual Trace CRO.
10. Draw the block dia. Of Digital storage CRO.
11. Give the Specification of CRO(any six)
12. State the application of CRO with e.g. of voltage measurement.

#### **4marks**

13 Draw the block dia. Of CRO & explain its working

14 Draw the diagram of vertical deflection system used in CRO & explain it.

15 Draw the block dia. Of Dual Beam CRO. & compare with single beam CRO.

16 Give the comparison between Dual trace & Dual beam CRO.(4 marks) State the procedure for measurement of following parameter using CRO.(lissajous pattern)

1. Phase

## 2. Frequency

### Chapter 5 Signal Generator & Wave Analyzer(20M)

#### 3marks

17 State & explain the two requirements of signal generator

18 Draw block diagram of RF/AF signal generator & explain its operation.

19 Draw the characteristics of a pulse generator.

20 Define a. rise time b. fall time c. settling time d. overshoot (any three)

21 Explain time & frequency domain analysis.

#### 4marks

22 Sketch neat labeled block diagram of function generator. & explain.

23 Explain the spectrum analyzer.

24 Explain the logic analyzer.